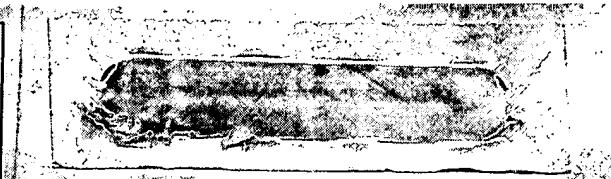


U.S. PTO
09/932381
08/17/01

Subclass	Class	ISSUE CLASSIFICATION



PATENT NUMBER

U.S. UTILITY Patent Application

A
SCANNED LM2 4/13 Q.A.

NO. I.P.T. 3

PATENT DATE

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/932381	F	711	14/4	2185	<i>Brasford</i> <i>M. K. Kim</i>

APPLICANTS
Serge Lässerre

TITLE: Local memory with indicator bits to support concurrent DMA and CPU access

PTO-2040
12/99

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ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
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TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent, subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent, No. _____				ISSUE FEE	
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